#### REMARKS

## Present Status of the Application

The Examiner rejected Claims 1-4, 6, 7, 9, 10, 12-14 under 35 U.S.C. 102(b), as being anticipated by Nishikawa et al. (US 6278503). The Examiner rejected Claims 5 and 11 under 35 USC 103(a) as being unpatentable over Nishikawa et al. (US 6,278,503) in view of Lee et al (US 20040257488). In addition, the Examiner stated claim 8 would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Applicant has amended claims 1, 9 and 14 and canceled 8 to more clearly define the present invention. The limitation of the allowable claim 8 has added to claim 1, and thus claim 1 and its dependent claims 2-7 should be allowed.

In addition, the limitation added in claim 9 is as shown in Figs. 2 and 5, the limitation added in claim 14 is as shown in Figs 2, 3, 6, 7, 8, and no new matter is entered. After entry of the foregoing amendments, claims 1-7 and 9-14 remain pending in the present application, and reconsideration of those claims is respectfully requested.

### Rejection under 35 U.S.C 102 (b)

The Examiner rejected Claims 1-4, 6, 7, 9, 10, 12-14 under 35 U.S.C. 102(b), as being anticipated by Nishikawa et al. (US 6278503).

Applicant has amended claim 1 with the limitation of the allowable claim 8, and thus claim 1 and its dependent claims 2-7 should be allowed.

In addition, Applicant respectfully submits that the independent claim 9 patently define over the prior reference for at least the reason that the cited art fails to disclose or suggest each and every feature as claimed in the present invention.

The present invention is in general related an active device array structure as claims 9 recites:

Claim 9. An active device array structure, the active device array structure comprising:

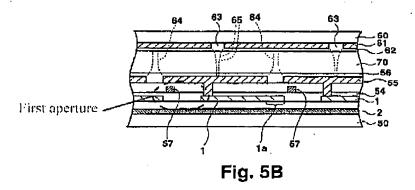
- a base plate;
- a plurality of gate lines disposed over the base plate;
- a plurality of data lines disposed over the base plate, wherein a pixel area is formed between any two adjacent gate lines and any two adjacent data lines;
- a plurality of active devices disposed over the base plate, wherein each active device is formed in an intersection region between the gate line and the data line and electrically connected to corresponding gate line and data line;
- a plurality of storage capacitors disposed over the base plate, wherein each storage capacitor has an upper electrode having at least a first aperture, and the first aperture is surrounded by the upper electrode; and

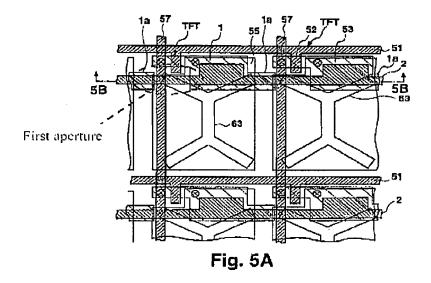
a plurality of pixel electrodes disposed over the pixel area, wherein each the pixel electrodes is respectively electrically connected to the corresponding active device and the corresponding upper electrode.

The office action stated Nishikawa has disclosed the upper electrode of each storage capacitor has at least a first aperture in Fig. 5B (signed as first aperture in the following figure 5B). However, applicant respectfully submits the first aperture in Fig. 5B of the citation is not an aperture surrounded by the electrode 1. Please see Fig. 5A shown below, the first aperture is just a space between the upper electrode 1 and the

#### electrode la in the adjacent pixel unit.

However, in claim 9 of the present invention, the first aperture is surrounded by the upper electrode. Nishikawa does not teach the SC electrode 1 has an aperture surrounded by the SC electrode 1.





For at least the foregoing reasons, Applicant respectfully submits Nishikawa does not teach each and every element in claim 9. Independent claim 9 is novel and

nonobvious to Nishikawa. Accordingly, Independent claim 9 patently define over the prior art reference, and should be allowed. For at least the same reasons, dependent claims 10-14 patently define over the prior art as a matter of law, for at least the reason that these dependent claims contain all features of their respective independent claim.

# Discussion of claim rejections under 35 USC 103(a)

The Office Action also rejected Claims 5 and 11 under 35 USC 103(a) as heing unpatentable over Nishikawa et al. (US 6,278,503) in view of Lee et al (US 20040257488).

Applicant respectfully submits the Lee reference (US 20040257488) is overcome. The Lee reference (US 20040257488) is filed on Mar. 31, 2004. However, the present application claimed a foreign priority benefit of Taiwan application serial no. 92119649, filed on July 18, 2003 under 35 U.S.C. 119. That is, the foreign priority date of the present application is earlier than the filling date of Lee (US 20040257488). Therefore, applicant respectfully submits the rejection of 103(a) should be withdrawn.

#### **CONCLUSION**

For at least the foregoing reasons, it is believed that all Claims 1-14 of the present application patently define over the prior art and are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

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Respectfully submitted,

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